

Betreff: [gdr-im] Call for Papers: 28th IEEE International Symposium on Computer Arithmetic (ARITH 2021)

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28th IEEE International Symposium on Computer Arithmetic

ARITH 2021

Virtual Conference, June 14-16, 2021

<http://arith2021.arithsymposium.org/>

CALL FOR PAPERS

The 28th edition of the symposium, ARITH 2021, which was originally scheduled in Turin, Italy, will go virtual due to the uncertainty of the world health situation and travels in early 2021.

Hence, for the first time ARITH will be a virtual conference with live presentations, keynote talks, and panels.

ARITH 2021 features two categories of submissions: Journal Papers (JPs), which will appear in a Special Section of IEEE Transactions on Emerging Topics in Computing (TETC) to be published tentatively in the conference dates (JIC2 model), and Conference Papers (CPs). The two categories have distinct submission, review and publication processes.

Refer to <http://arith2021.arithsymposium.org/call.html> for detailed information.

Authors are invited to submit papers describing recent advances on all aspects related to computer arithmetic, its applications or implementations. This includes, but is not restricted to, the following topics:

- * Foundations of computer arithmetic: innovative number systems and their applications, emerging arithmetic algorithms and their analysis and applications, elementary function algorithms for emerging architectures and applications.
- * Efficient, low-power and novel implementations of computer arithmetic: processors and units, elementary and special functions, multiple-precision, interval arithmetic, etc.
- * Floating-point units and algorithms, properties of floating-point arithmetic in emerging domains and applications.
- * Standards, high-level language and compiler impact on emerging arithmetic systems.
- * Test, verification, formal proof, computer aided design (CAD) automation and fault/error-tolerance for computer arithmetic emerging architectures and implementations.
- * New arithmetic paradigms and architectures for FPGA's or configurable logic.
- * New arithmetic paradigms and architectures for specific application domains such as cryptography, security, neural networks, deep learning, signal processing, computer graphics, multimedia, computer vision, distributed and parallel computing (e.g., HPC), finance, etc.

- * Computer arithmetic for emerging technologies and non-conventional computer arithmetic.
- * Inexact and stochastic arithmetic algorithms, architectures and applications.
- * New arithmetic paradigms (algorithms, architectures, implementations) for emerging technologies.

IMPORTANT DATES

Journal Papers Submissions:

Paper submission deadline	Nov. 1, 2020
Authors notifications	Feb. 1, 2021
Submission of revised papers (only minor revisions)	Feb. 22, 2021
Notification of final acceptance	Mar. 8, 2021
Camera ready submission	Mar. 31, 2021

Conference Papers Submissions:

Abstract submission deadline	Feb. 1, 2021
Complete paper submission deadline	Feb. 8, 2021
Authors notifications	April 2021
Camera ready and copyright due	May 2021

Refer to <http://arith2021.arithsymposium.org/call.html> for detailed information.

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